



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Kudo et al.

Serial No.: 09/872,007

Group Art Unit:

2822

Filed: June 04, 2001

Examiner:

Soward, Ida M.

For: FABRICATION OF LOW POWER CMOS DEVICE WITH HIGH RELIABILITY

Honorable Commissioner of Patents

Box AF

Washington, D.C. 20231

AMENDMENT UNDER 37 C.F.R. §1.116

Sir:

In response to the Office Action dated January 29, 2003, please amend the above-identified application as follows:

INTRODUCTORY COMMENTS

Claims 1 and 3-19 are all the claims presently pending in the application. Claims 1 and 3-5, 8 and 12-17 have been amended to more particularly define the invention and claim 2 has been canceled. Claims 3-19 have been withdrawn from prosecution. Of the remaining claims, claim 1 is independent.

Applicants thank Examiner Soward for the courtesies extended to the Applicants' representative during a telephone interview February 12, 2003. During that personal interview, Applicants' representative explained that the second and third MOSFETs form a single CMOS pair and that none of the applied references discloses or suggests this feature. The Examiner agreed that the applied references do not appear to disclose that feature. Therefore, in accordance with that agreement, this Amendment amends independent claims 1 and 3 to recite that feature.

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